

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	display and pixel array and scanning driver circuit and data driver circuit and display control circuit and horizontal scanning period and first direction and second direction and frame period and time-sequential interval and scanning signal	US-PGPUB	ADJ	ON	2006/09/18 08:18

Interference Search History
Print out.

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	display and pixel array and scanning driver circuit and data driver circuit and display control circuit and horizontal scanning period and first direction and second direction and frame period and time-sequential interval and scanning signal	US-PGPUB	ADJ	ON	2006/09/18 08:21
L2	9877	display and pixel array	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/09/18 08:19
L3	26	display and pixel array and scanning driver circuit and data driver circuit	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/09/18 08:19
L4	6	display and pixel array and scanning driver circuit and data driver circuit and display control circuit and horizontal scanning period	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/09/18 08:19
L5	6	display and pixel array and scanning driver circuit and data driver circuit and display control circuit and horizontal scanning period and first direction and second direction	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/09/18 08:19
L6	6	display and pixel array and scanning driver circuit and data driver circuit and display control circuit and horizontal scanning period and first direction and second direction and frame period	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/09/18 08:20
L7	1	display and pixel array and scanning driver circuit and data driver circuit and display control circuit and horizontal scanning period and first direction and second direction and frame period and time-sequential interval	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/09/18 08:20

EAST Search History

L8	1	display and pixel array and scanning driver circuit and data driver circuit and display control circuit and horizontal scanning period and first direction and second direction and frame period and time-sequential interval and scanning signal	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/09/18 08:20
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